

Fig. 1

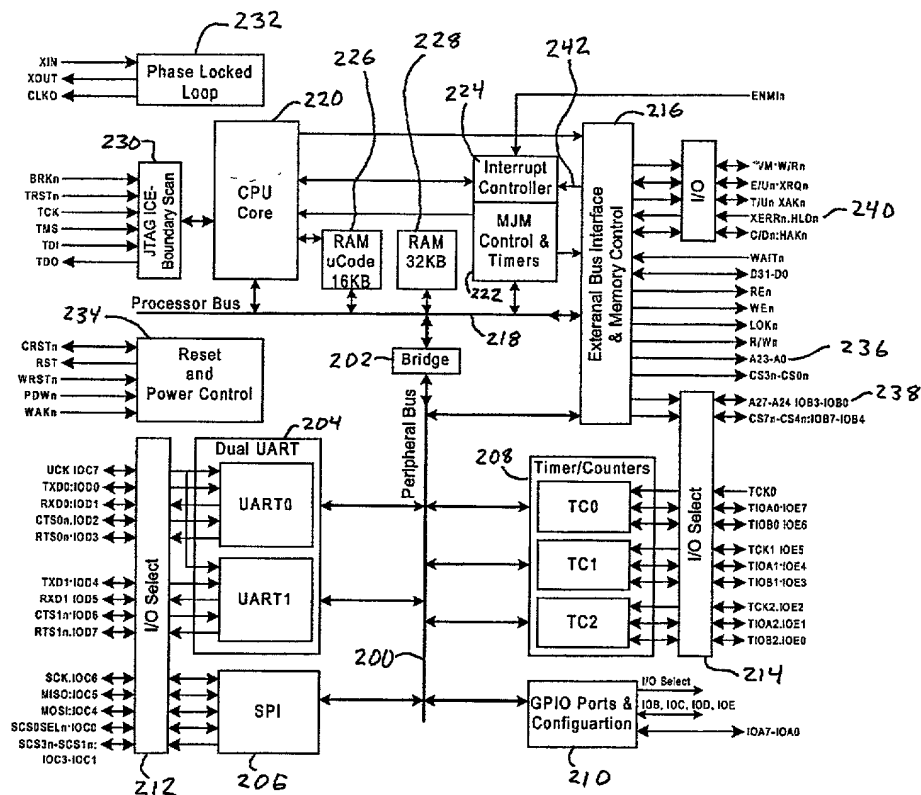


Fig. 2

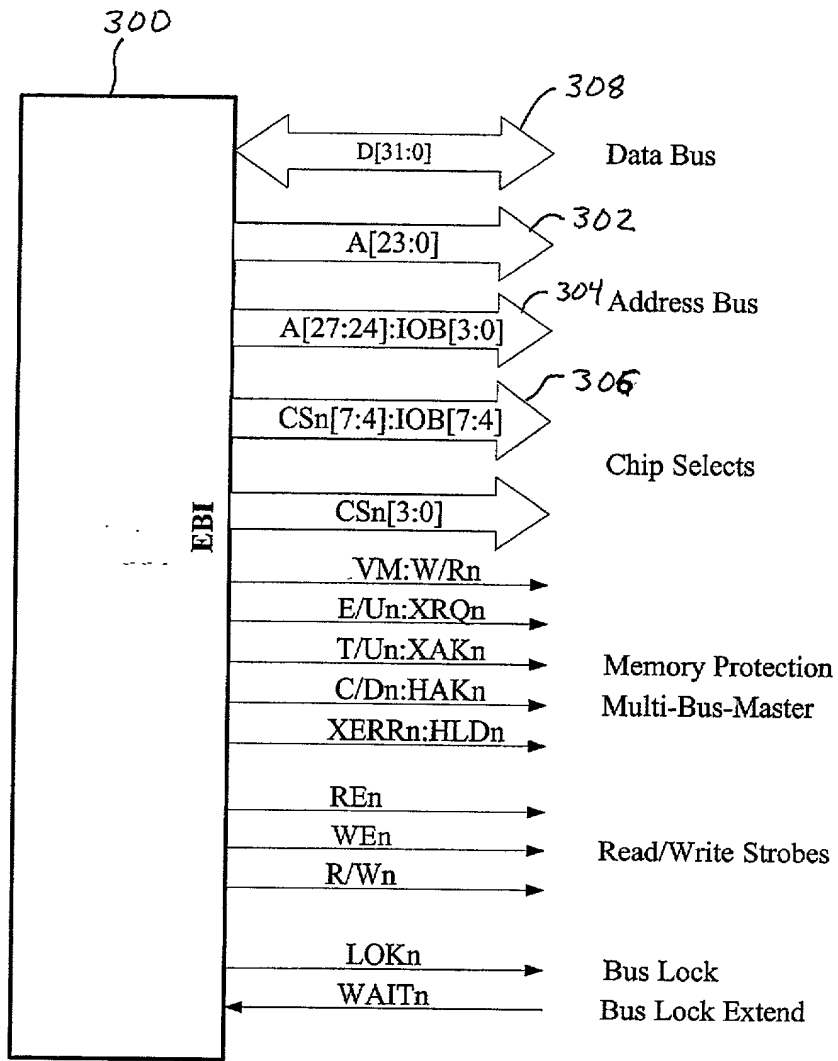


Fig. 3

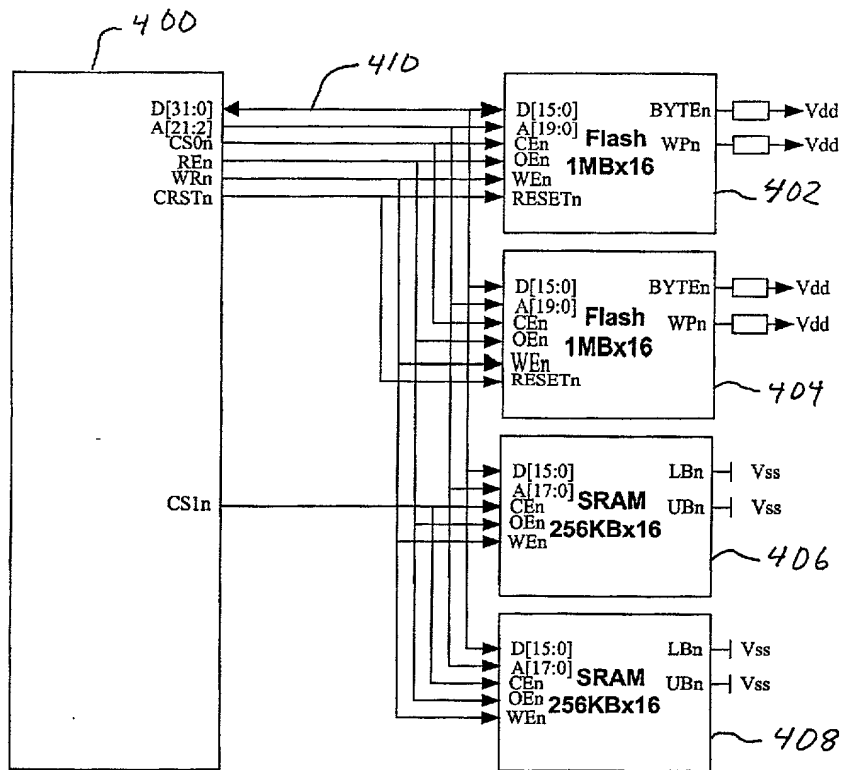


Fig. 4

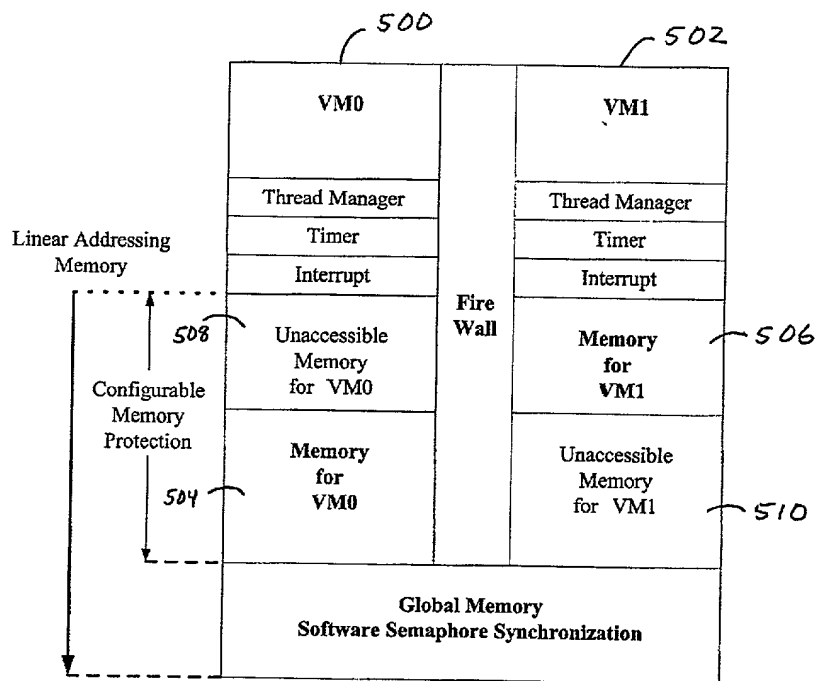


Fig. 5

VM,T/Un		CS1n,CS0n			
		00	01	10	11
00	x	x	x	x	x
01	0	1	1	1	1
10	1	1	0	1	1
11	1	1	1	1	1

Fig. 6

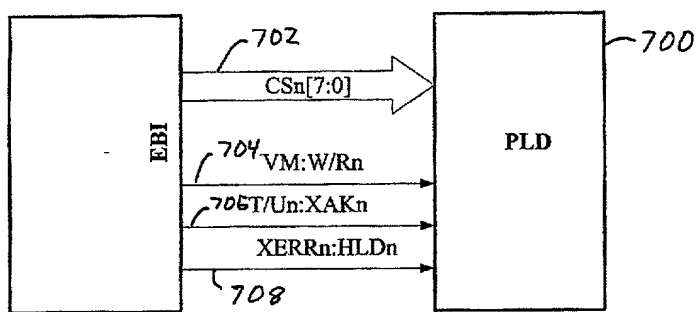


Fig. 7

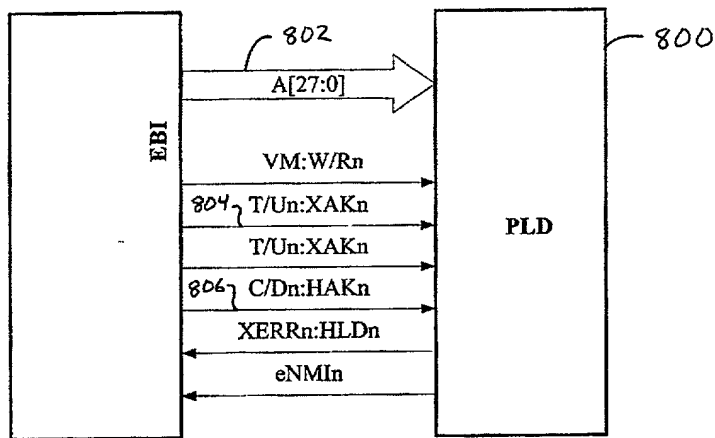


Fig. 8

FOR FURTHER INFORMATION

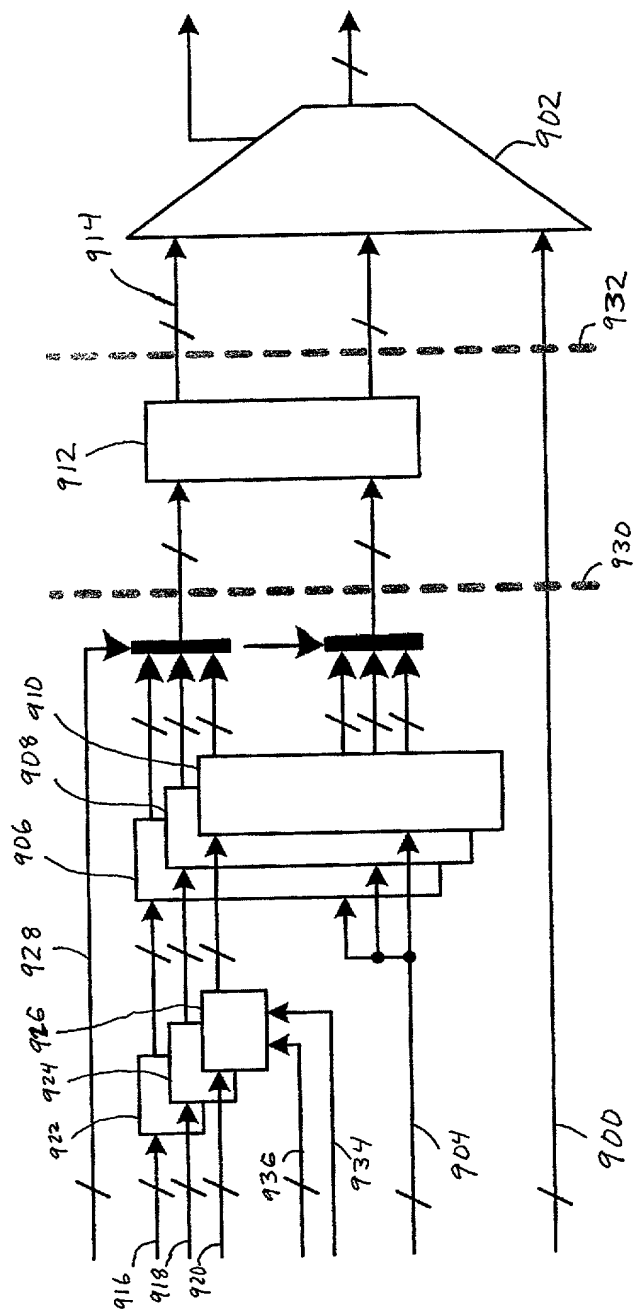


Fig. 9

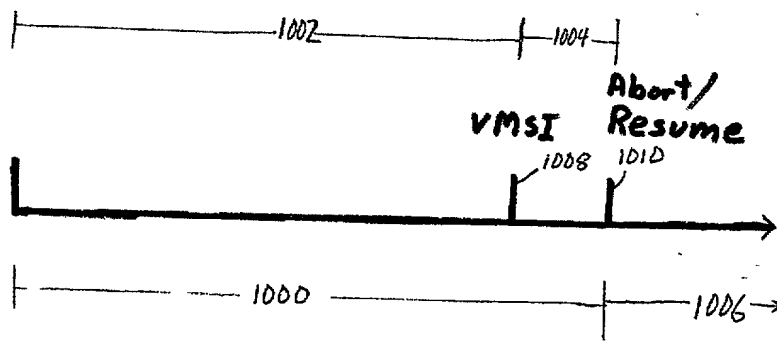


Fig. 10

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